

Ready

EAST - (714553.wsp.1)

File View Edit Tools Window Help

Drafts Pending Active Failed Saved Favorites Tagged (4)

Source: DB: Default operator:

☐ BRS form ☐ ISIR form ☐ Image ☐ Text ☐ HTML

☐ Drafts ☐ Pending ☐ Active ☐ Failed ☐ Saved ☐ Favorites ☐ Tagged (4)

☐ Drafts ☐ Pending ☐ Active ☐ Failed ☐ Saved ☐ Favorites ☐ Tagged (4)

☐ Drafts ☐ Pending ☐ Active ☐ Failed ☐ Saved ☐ Favorites ☐ Tagged (4)

Type	L #	Hits	Search Text	DBs	Time	C	Err	Er
68	BRS	L32	19	382/260-266.ccls. and fpga and filter\$2	USPAT	2005		
69	BRS	L33	8	382/275.ccls. and fpga and filter\$2	USPAT	2005		
70	BRS	L34	5	382/298.ccls. and fpga and filter\$2	USPAT	2005		
71	BRS	L36	6	358/1.2.ccls. and fpga and filter\$3	USPAT	2005		
72	BRS	L42	24	olympus and fukuchi	USPAT	2005		
73	BRS	L54	31	fpga same filter\$3 same configuration	USPAT	2005		
74	BRS	L57	13	MIZUNO-HIDEAKI.in.	USPAT	2005		
75	BRS	L58	6	TORIYAMA-HIDEYUKI.in.	USPAT	2005		
76	BRS	L59	12	KAMEI-NOBUO.in.	USPAT	2005		
77	BRS	L60	5	YONEYAMA-TSUYOSHI.in.	USPAT	2005		
78	BRS	L69	9	SUZUKI-HIROYUKI.in. and filter\$3 and configu\$6	USPAT	2005		
79	BRS	L70	2	TORIYAMA-HIDEYUKI.in. and filter\$3 and configu\$6	USPAT	2005		
80	BRS	L72	16	SUZUKI-HIROYUKI.in. and "382"/\$5.ccls.	USPAT	2005		
81	BRS	L79	15	(area adj determination) and isolated and matrix and filter\$3	USPAT	2005		
82	BRS	L80	42	filter\$3 with configuration with memory with circuit	USPAT	2005		
83	BRS	L82	24	filter\$3 with configuration with fifo	USPAT	2005		
84	BRS	L84	3	olympus and (field with gate) and (circuit adj data)	USPAT	2005		
85	BRS	L85	2	(olympus and (field with gate) and (circuit adj data)) and filter\$3	USPAT	2005		
86	BRS	L97	2	masami-f\$.in.	US-PGP	2005		
87	BRS	L10	78	field and gate and array and filter	DERWEN	2005		
88	BRS	L10	5	yurt-p\$.in.	USPAT	2005		
89	BRS	L10	18	382/312-314.ccls. and duy	USPAT	2005		
90	BRS	L10	47	(fpga and filter\$3 and pixel\$2 and (fifo or memory) and configuration and circui	USPAT	2005		
91	BRS	L11	93	(filter\$3 and (pixel\$2 adj matrix) and (fifo or memory) and configuration and ci	USPAT	2005		
92	BRS	L11	5	fpga and filter\$3 and (pixel\$2 adj matrix) and (fifo or memory) and configuratio	USPAT	2005		
93	BRS	L11	5	filter\$3 and (pixel\$2 adj matrix) and (fifo or memory) and (circuit near configu	USPAT	2005		
94	BRS	L11	7	rewrit\$4 same configuration same memory same filter	USPAT	2005		
95	BRS	L12	13	MIZUNO-HIDEAKI.in.	USPAT	2005		
96	BRS	L12	6	TORIYAMA-HIDEYUKI.in.	USPAT	2005		
97	BRS	L12	12	KAMEI-NOBUO.in.	USPAT	2005		
98	BRS	L12	5	YONEYAMA-TSUYOSHI.in.	USPAT	2005		
99	BRS	L46	128	fukuchi-m\$.in.	USPAT	2005		
100	BRS	L11	114	rom and cpu and gate and array and fifo and writ\$3 and mode and (circuit adj dat	USPAT	2005		
101	BRS	L11	276	SUZUKI-HIROYUKI.in.	USPAT	2005		
102	BRS	L45	365	fukuchi-\$5.in.	USPAT	2005		

Ready